

**ReleaseOrder ID:** SCGCQ01686850  
**Headline:** GCA Release: LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0 - 26.00.00.00-1  
**Release Version:** 26.00.00.00-1  
**UCM Project:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0  
**UCM Stream:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0\_Rel  
**Release Type:** GCA  
**State:** Deployed  
**Release Baseline:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0-2018-04-05-26.00.00.00-1\_REL\_1522984313@ISAS35  
**Release Date:** 26-APR-18  
**Date Generated:** Oct 01, 2019

## Release History

- [SCGCQ01630231 - Beta Release: LINUX\\_RH\\_SL\\_OEL\\_CTX\\_MPT\\_GEN35\\_PHASE\\_7.0 - 25.255.05.00-1](#)
- [SCGCQ01613865 - Beta Release: LINUX\\_RH\\_SL\\_OEL\\_CTX\\_MPT\\_GEN35\\_PHASE\\_7.0 - 25.255.04.00-1](#)
- [SCGCQ01567463 - Pre-Alpha 3 Release: LINUX\\_RH\\_SL\\_OEL\\_CTX\\_MPT\\_GEN35\\_PHASE\\_7.0 - 25.255.03.00-1](#)
- [SCGCQ01561803 - Pre-Alpha2 Release: LINUX\\_RH\\_SL\\_OEL\\_CTX\\_MPT\\_GEN35\\_PHASE\\_7.0 - 25.255.02.00-1](#)
- [SCGCQ01550672 - Pre-Alpha Release: LINUX\\_RH\\_SL\\_OEL\\_CTX\\_MPT\\_GEN35\\_PHASE7.0 - 25.255.01.00-1](#)

**ReleaseOrder ID:** [SCGCQ01630231](#) Open In CQWeb  
**Headline:** Beta Release: LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0 - 25.255.05.00-1  
**Release Version:** 25.255.05.00-1  
**UCM Project:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0  
**UCM Stream:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0\_Rel  
**Release Type:** Beta  
**State:** Test\_Complete  
**Release Baseline:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0-2018-03-14-25.255.05.00-1\_REL\_1521023845@ISAS35  
**Release Date:** 29-MAR-18  
**Date Generated:** Oct 01, 2019

### Defects Fixed (1):

ID: SCGCQ01629402

**Headline:** Ventura: As per MPI spec use Supplemental Reply Post Host Index Register(combined reply queue) when MSI-X vectors > 16

**Description Of Change:** Modified driver code to use Supplemental Reply Post Host Index Register(combined reply queue) if firmware supports more than 16 MSI-X vectors.

**Issue Description:** For Ventura, driver is using combined reply queue feature when MSIX-vectors > 8 instead it should use this feature only when MSIX-vectors > 16.

**Steps To Reproduce:** NA

**ReleaseOrder ID:** [SCGCQ01613865](#) Open In CQWeb  
**Headline:** Beta Release: LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0 - 25.255.04.00-1  
**Release Version:** 25.255.04.00-1  
**UCM Project:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0  
**UCM Stream:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0\_Rel  
**Release Type:** Beta  
**State:** Superseded  
**Release Baseline:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0-2018-02-28-25.255.04.00-1\_REL\_1519822934@ISAS35  
**Release Date:** 29-MAR-18  
**Date Generated:** Oct 01, 2019

### Enhancements Implemented (2):

ID: SCGCQ01606945

**Headline:** Upstream mpt3sas: scsi\_transport\_sas: switch to bsg-lib for SMP passthrough

**Description Of Change:** This patch simplified the SMP passthrough code by switching it to the generic bsg-lib helpers that abstract away the details of the request code, and gets drivers out of seeing struct scsi\_request.

ID: SCGCQ01607482

**Headline:** Upstream mpt3sas: Fix function prototypes for module\_param\_call()

**Description Of Change:** Function prototypes for the set/get functions defined by module\_param\_call() have a slightly wrong argument types. This patch fixed by passing correct argument types in function prototypes for the set/get functions defined by module\_param\_call().

**ReleaseOrder ID:** [SCGCQ01567463](#) Open In CQWeb  
**Headline:** Pre-Alpha 3 Release: LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0 - 25.255.03.00-1  
**Release Version:** 25.255.03.00-1  
**UCM Project:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0  
**UCM Stream:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0\_Rel  
**Release Type:** Alpha  
**State:** Test\_Complete  
**Release Baseline:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0-2018-01-19-25.255.03.00-1\_REL\_1516370997@ISAS35  
**Release Date:** 22-JAN-18  
**Date Generated:** Oct 01, 2019

### Defects Fixed (1):

ID: SCGCQ01555936

**Headline:** Fix, False timeout prints for ioctl and other internal commands during controller reset.

**Description Of Change:** Introduced function "mpt3sas\_base\_check\_cmd\_timeout" to check command got time out or terminated due to Host reset and prints accordingly.

**Issue Description:** In below case we can see false timeout prints in driver.  
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If ioctl is send to FW and if there is a controller reset issued before ioctl gets completed,  
In controller reset path, the pending ioctl commands are terminated from "mpt3sas\_ctl\_reset\_handler" function.  
This will wake up the waiting ioctl commands in ioctl path, and prints timeout which is actually not a timeout.

**Steps To Reproduce:** NA

## Enhancements Implemented (2):

**ID:** SCGCQ01541636

**Headline:** Upstream mpt3sas: fix dma\_addr\_t casts

**Description Of Change:** Changes of upstream posted patch "mpt3sas: SGL to PRP Translation for I/Os to NVMe devices" done in order to keep the code in sync with upstream(though no change in functionality b/w upstream and inhouse but code semantics/implementation was different) and this patch leads to build warning on 32bit configurations, its because it mixes up pointers and dma\_addr\_t values unnecessarily.

Modified code to use the correct types consistently, which lets us get rid of a lot of type casts in the process. Also renamed some variables to avoid confusion between physical and dma address spaces that are often distinct.

**ID:** SCGCQ01554011

**Headline:** Added support for XenServer 7.3 OS.

**Description Of Change:** Since kernel version of XenServer 7.3 is same as XenServer 7.2 (4.4.0+10), mpt3sas driver RPM avago-mpt3sas CitrixXen7.2 will work on XenServer7.3 also.

Updated this information in README as well as included "CitrixXen7.3\_README.txt" XenServer7.3 specific README in Citrix7 folder of Release folder.

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**ReleaseOrder ID:** [SCGCQ01561803](#) [Open In CQWeb](#)  
**Headline:** *Pre-Alpha2 Release: LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0 - 25.255.02.00-1*  
**Release Version:** 25.255.02.00-1  
**UCM Project:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0  
**UCM Stream:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0\_Rel  
**Release Type:** Pre-Alpha  
**State:** Test Complete  
**Release Baseline:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE\_7.0-2018-01-12-25.255.02.00-1\_REL\_1515742850@  
SAS35  
**Release Date:** 05-FEB-18  
**Date Generated:** Oct 01, 2019

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## Defects Fixed (1):

**ID:** SCGCQ01558459

**Headline:** Code refactor: memory allocation, setting status for incorrect command, accessing dev struct after decrementing its ref count.

**Description Of Change:** 1.Modified driver code to pass "sizeof(struct MPT3SAS\_TARGET)" while allocating memory for member of "struct MPT3SAS\_TARGET"

2.In \_scsih\_scan\_finished driver should mark port\_enable\_cmd.status as 'command not used', instead base\_cmds.status was marked as 'command not used'.  
so updated code to mark port\_enable\_cmd.status as 'command not used'.

3.In slave configure of NVMe handling, member of \_pcie\_device structure was accessed after its reference count is decremented/put.  
Modified code to access member of \_pcie\_device structure before its reference count is decremented/put.

**Issue Description:** Below are the defects being addressed:

- 1.Mismatch in sizeof(arg) while allocating memory for structure instance.
- 2.Incorrect command status was set/marked as not used.
- 3.\_pcie\_device structure being referenced after decrementing its reference count.

**Steps To Reproduce:** NA

## Enhancements Implemented (1):

**ID:** SCGCQ01558613

**Headline:** Skip pending I/Os issued to NVMe drives while processing Broadcast Async Event

**Description Of Change:** The Linux driver when receives Broadcast Asynchronous Event Notification (BAEN) from the controller firmware, checks all pending I/Os at the driver level and issues query task, abort task TMs. This is done in the driver to handle drives which are connected with multiple initiators and undergoing target resets. In the BAEN handling code, the I/Os issued to NVMe drives are also handled and query task and abort task TMs are issued, which are not necessary as there is no multi-initiator and no concept BAEN with NVMe drives. Hence when the drive checks for pending I/Os it skips I/Os issued to NVMe drives.

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**ReleaseOrder ID:** [SCGCQ01550672](#) [Open In CQWeb](#)  
**Headline:** *Pre-Alpha Release: LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE7.0 - 25.255.01.00-1*  
**Release Version:** 25.255.01.00-1  
**UCM Project:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE7.0  
**UCM Stream:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE7.0\_Rel  
**Release Type:** Pre-Alpha  
**State:** In\_Review  
**Release Baseline:** LINUX\_RH\_SL\_OEL\_CTX\_MPT\_GEN35\_PHASE7.0-2017-12-27-25.255.01.00-1\_REL\_1514440994@  
SAS35  
**Release Date:** 11-JAN-18  
**Date Generated:** Oct 01, 2019

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## Enhancements Implemented (1):

**ID:** SCGCQ01541799

**Headline:** Upstream mpt3sas: remove redundant copy\_from\_user in \_ctl\_getiocinfo

**Description Of Change:** Remove copy from user in function ctl\_getiocinfo in ioctl path. After the user copy, we are going to memset().So the copy\_from\_user is redundant.